

WEST Search History for Application 10783216

Creation Date: 2009013019:07

Query	DB	Op.	Plur.	Thes.	Date
(6114843 5568062)[pn]	PGPB, USPT	OR	YES		01-30-2009
(pld or "programmable logic device" or fpga or "field programmable gate array")	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array")) and (blocks near3 active)	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array")) and ((blocks near3 active) same path)	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array") and ((blocks near3 active) same path)) and slack	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array") and (blocks near3 active)) and slack	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array")) and (slack with minimum)	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array")) and (assign\$ with voltage)	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array") and (assign\$ with voltage)) and slack	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array")) and (timing near2 analysis)	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array") and (timing near2 analysis)) and slack	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array") and (timing near2 analysis) and slack) and voltage	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array")) and (voltage near2 regulator)	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array") and (voltage near2 regulator)) and (operating near3 voltage)	PGPB, USPT	OR			01-30-2009

((pld or "programmable logic device" or fpga or "field programmable gate array") and (voltage near2 regulator) and (operating near3 voltage)) and slack	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array") and (voltage near2 regulator)) and slack	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array")) and ((control\$ or variable or regulat\$) near5 voltage)	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array") and ((control\$ or variable or regulat\$) near5 voltage)) and slack	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array") and (slack with minimum)) and voltage	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array")) and (voltage near2 (switch or switches))	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array") and (voltage near2 (switch or switches))) and slack	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array")) and ((first and second) near3 (supply or voltage))	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array") and ((first and second) near3 (supply or voltage))) and slack	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array")) and (operating near3 voltage)	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array") and (operating near3 voltage)) and slack	PGPB, USPT	OR			01-30-2009
((programmable adj logic adj device) or pld or (field adj programmable adj gate adj array) or fpga) same (sleep adj transistor)	PGPB, USPT	OR			01-30-2009
(logic near block) with (sleep adj transistor)	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array"))) and (enabl\$ with voltage)	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array")) and (enabl\$ with voltage)) and	PGPB, USPT	OR			01-30-2009

slack					
((pld or "programmable logic device" or fpga or "field programmable gate array"))) and (select\$ with voltage)	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array")) and (select\$ with voltage)) and slack	PGPB, USPT	OR			01-30-2009
(326/80)[ccls]	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array"))) and ((326/80)[ccls])	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device" or fpga or "field programmable gate array")) and (326/80)[ccls]) and slack	PGPB, USPT	OR			01-30-2009
(operat\$ with (pld or "programmable logic device"))[clm]	PGPB, USPT	OR			01-30-2009
(pld or "programmable logic device")[clm]	PGPB, USPT	OR			01-30-2009
"timing analysis"[clm]	PGPB, USPT	OR			01-30-2009
"active blocks"[clm]	PGPB, USPT	OR			01-30-2009
("active blocks"[clm]) and path[clm]	PGPB, USPT	OR			01-30-2009
slack[clm]	PGPB, USPT	OR			01-30-2009
(minimum with slack)[clm]	PGPB, USPT	OR			01-30-2009
(enabl\$ with assignment)[clm]	PGPB, USPT	OR			01-30-2009
(first adj2 (supply or voltage))[clm]	PGPB, USPT	OR			01-30-2009
(second adj2 (supply or voltage))[clm]	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device")[clm]) and ("timing analysis"[clm]) and (slack[clm]) and ((first adj2 (supply or voltage))[clm]) and ((second adj2 (supply or voltage))[clm])	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device")[clm]) and ((voltage or supply) adj2 terminal)[clm]	PGPB, USPT	OR			01-30-2009

((pld or "programmable logic device")[clm] and ((voltage or supply) adj2 terminal)[clm]) and ((minimum with slack)[clm])	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device")[clm]) and "voltage regulator"[clm]	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device")[clm] and "voltage regulator"[clm]) and variable[clm]	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device")[clm]) and (select\$ with "operating voltage")[clm]	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device")[clm]) and (control\$ with "voltage regulator")[clm]	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device")[clm]) and ((first adj2 (supply or voltage))[clm]) and ((second adj2 (supply or voltage))[clm])	PGPB, USPT	OR			01-30-2009
((pld or "programmable logic device")[clm] and (first adj2 (supply or voltage))[clm] and (second adj2 (supply or voltage))[clm]) and ("voltage switch\$")[clm]	PGPB, USPT	OR			01-30-2009